

Notice of Allowability

Application No.

10/764,511

Examiner

A Elamin

Applicant(s)

SASAGAWA, YUKIHIRO

Art Unit

2116

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Application filed on 1/27/2004.
2. ☒ The allowed claim(s) is/are 1-11.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 1/27/2004
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-11 are allowed.

REASONS FOR ALLOWANCE

2. The following is an examiner's statement of reasons for allowance:

The prior art of record does not teach or suggest individually or in combination the following:

a first instruction set including instruction execution control function information in a specific bit field of an instruction code inputted to a microprocessor, the information being used for selecting a flag for an instruction execution control function, and one or more second instruction sets including control specification information for low power control in the specific bit field, a control specification information extraction circuit for extracting control specification information of a control circuit for reading the control specification information in a second instruction set mode and performing a low power operation, wherein the instruction execution control function information and the control specification information are provided beforehand in each program for executing an instruction, and a low power operation of each control circuit is performed for each instruction according to the control specification information, thereby controlling a low power operation of the microprocessor for each instruction.

For these reasons claims 1, 2 are deemed to be allowable over the prior art of record, and

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claims 3-9 are allowable by dependency.

Also the prior art of record does not teach or suggest individually or in combination the following:

a first instruction set including instruction execution control function information in a specific bit field of an instruction code inputted to a microprocessor, the information being used for selecting a flag for an instruction execution control function, and one or more second instruction sets including control specification information for low power control in the specific bit field, a control specification information extraction circuit for extracting control specification information of a control circuit for reading the control specification information in a second instruction set mode and performing a low power operation, a program throughput measuring unit for measuring a throughput on a specific point of a program by using the control specification information; and a throughput referring unit for making reference to the throughput as a variable in a program of the microprocessor, wherein reference is made, during program execution, to a program throughput obtained for each instruction of a program operation.

For these reasons claims 10-11 are deemed to be allowable over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

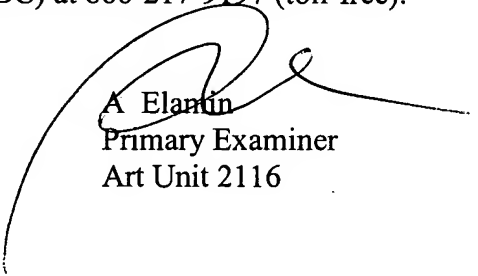
Any inquiry concerning this communication or earlier communications from the

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examiner should be directed to A Elamin whose telephone number is (571) 272-3674. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (571) 272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



A Elamin
Primary Examiner
Art Unit 2116

April 17, 2006